## Cies \& safety

## Time-delay relays from riese



| Applications | Relay designations (all with CE) | Column | EN 954 safety class |
| :---: | :---: | :---: | :---: |
| On-delay | RS-VR1, RS-VR2, <br> RS-ZR1, RS-ZR2, RS-ER1, RS-ER2, <br> RS-ERF, RS-EB, RS-LR1, <br> RS-LR2, RS-LZ1, RS-LZ2, RS-LZF | $\begin{aligned} & 1 \\ & 2,3 \\ & 4,5,6 \\ & 7,8 \end{aligned}$ | $\begin{aligned} & 1 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ |
| Off-delay | RS-AR1, RS-AR2, RS-AZ1, RS-AZ2, RS-LA1, RS-LA2, RS-AZS | $\begin{aligned} & 9,10 \\ & 11,12 \end{aligned}$ | $\begin{aligned} & 1 \\ & 1 \end{aligned}$ |
| Flashing | RS-BR1, RS-BR2, RS-BRS1, RS-BRS2 | 13, 14 | 1 |
| Clock-pulsed | RS-TG1, RS-TG2, RS-TGI1, RS-TGI2 | 15,16 | 1 |
| Multifunction | RS-MF | 17 | 1 |
| Interval time-delay | RS-EI1, RS-EI2, RS-EIF, RS-WAR, RS-WAR2 | $\begin{aligned} & 18,19, \\ & 20 \end{aligned}$ | $\begin{aligned} & 1 \\ & 1 \end{aligned}$ |
| Star-delta | RS-SD, RS-SDS | 21, 22 | 1 |

Time-delay On-delay

|  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Type | RS-VR1, RS-VR2 | RS-ZR1, RS-ZR2 | RS-ER1, RS-ER2 | RS-ERF | RS-EB | RS-LR1, RS-LR2 | RS-LZ1, RS-LZ2 | RS-LzF |
| Function/ Contact | On-delay 1 time-delay (RS-VR1) 2 time-delay (RS-VR2) | On-delay <br> 1 time-delay (RS-ZR1) <br> 2 time-delay (RS-ZR2) | On-delay <br> 1 time-delay (RS-ER1) <br> 2 time-delay (RS-ER2) | On-delay <br> 1 time-delay and <br> 1 remote connection | On-delay 1 thyristor | On-delay <br> 1 time-delay (RS-LR1) <br> 2 time-delay (RS-LR2) | On-delay <br> 1 time-delay (RS-LZ1) <br> 2 time-delay (RS-LZ2) | On-delay <br> 1 time-delay and <br> 1 remote connection |
| Pulse schedulel Function diagram |  |  |  | $\left[\begin{array}{l} \text { Voltage } \\ \text { supply } \\ \text { sif } \\ \text { ont } \\ \text { Output closed } \\ \text { relay } \\ \text { open } \end{array}\right)$ |  |  |  | $-2$ |
| Wiring diagram | RS-VR1 <br> RS-VR2 |  |  |  |  |  |  |  |
| LED | No | 2 LEDs | 2 LEDs | 2 LEDs | 1 LED | 2 LEDs | 2 LEDs | 2 LEDs |
| Timing ranges |  |  | $0.05-1 \mathrm{~s}$  <br> $0.15-$ 3 s <br> 0.5 -10 s <br> 1.5 -30 s <br> 3 - <br> 5 -100 s <br> 5 -300 s <br> 15 -300 s | $0.05-1 \mathrm{~s}$  <br> $0.15-$ 3 s <br> 0.5 -10 s <br> 1.5 - <br> 3 -60 s <br> 5 -100 s <br> 15 -300 s <br> 30 -600 s | $\left\lvert\, \begin{array}{rr} 0.05 & -1 \mathrm{~s} \\ 0.15 & - \\ 0.5 & 3 \mathrm{~s} \\ 0 . & 10 \mathrm{~s} \\ 1.5 & - \\ 3 & -60 \mathrm{~s} \\ 5 & - \\ 50 & -100 \mathrm{~s} \\ 15 & -300 \mathrm{~s} \\ 30 & -600 \mathrm{~s} \end{array}\right.$ |  | $1.5-$ 30 min <br> $3-$ 60 min <br> $0.15-$ 3 h <br> 0.5  <br>  10 h | $1.5-30 \mathrm{~min}$   <br> 3 - 60 min <br> $0.15-$ 3 h  <br> $0.5-$ 10 h  |
| Dimensions | see diagram | see diagram | see diagram | see diagram | see diagram | see diagram | see diagram | see diagram |
| Exciting voltage | $\begin{array}{rl} 12 & \vee \mathrm{AC} / \mathrm{DC} \\ 24 \mathrm{VAC/DC} \\ 42-48 & \mathrm{VAC} \\ 110-127 & \mathrm{VAC} \\ 230 & \mathrm{VAC} \\ \hline \end{array}$ | $\begin{array}{rl} 12 & \vee \mathrm{AC} / \mathrm{DC} \\ 24 \mathrm{AC/DC} \\ 42-48 & \mathrm{ACC} \\ 110-127 & \mathrm{AAC} \\ 230 & \vee \mathrm{AC} \\ \hline \end{array}$ | $12 \mathrm{VAC} / D C$ $24 \mathrm{VAC} / D C$ $42-48 \mathrm{VAC}$ $110-127 \mathrm{VAC}$ 230 | $24 \mathrm{~V} \mathrm{AC/DC}$ $42-48 \mathrm{VAC}$ $110-127 \mathrm{VAC}$ 230 VAC | 10-250 V AC/DC with jumper e1-e2: $10-60$ V AC/DC, without jumper: 40-250 V AC/DC | 12 V AC/DC 24 VACIDC $42-48 \vee \mathrm{AC}$ $110-127 \mathrm{VAC}$ 230 | $12 \vee \mathrm{AC} / \mathrm{DC}$ 24 VACIDC $42-48 \vee \mathrm{AC}$ $110-127 \mathrm{VAC}$ $230 \vee \mathrm{AC}$ | $12 \mathrm{VAC} / D C$ $24 \mathrm{VAC} / D C$ $42-48 \mathrm{VAC}$ $110-127 \mathrm{VAC}$ 230 VAC |
| Tolerances | 10\% | 10\% | 4\% | 4\% | 4\% | 10\% | 4\% | 4\% |
| Attributes | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale | $\begin{aligned} & \hline \text { CMOS technology } \\ & \text { Analog time setting } \\ & \text { Absolute scale } \\ & \hline \end{aligned}$ |
| Description of function | The time lag begins after applying the exciting voltage to terminals A1 und A2. The output relay attracts and remains in this switched position until the exciting voltage is interrupted. When the exciting voltage is interrupted, the output relay returns to its normal position. If the pulse control is shorter than reset time, it does not cause that the output relay switches back through instantaneously. | see col. 1 | see col. 1 | Resistance value for all time ranges is $10 \mathrm{~K} \Omega$. The factoryfitted jumper has to be removed when connecting an external potentiometer for setting the time. The remote potentiometer has to be connected to terminals Z1 and $Z 2$. The integrated potentiometer has to be set to the shortest time. The lines to the external potentiometer can be up to 100 m long. Note: there is no electrical isolation between terminals Z1 and Z2 and the exciting voltage A1 and A2. | This is an on-delay time-lag relay in 2-wire technology (binary contact). The power supply for the electronics is achieved via the load. The time lag begins after applying the exciting voltage to the series circuit of timing element and load (contactor). After the time has expired, the semiconductor output of the timing element becomes conductive and the load is energized. If the pulse control is shorter than reset time, this does not lead to instantaneous energizing on of the load. | see col. 1 | see col. 1 | see col. 4 |
| Column | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |

Time-delay Off-delay

|  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Type | RS-AR1, RS-AR2 | RS-AZ1, RS-AZ2 | RS-LA1, RS-LA2 | RS-AZS |
| Function/ Contact | Off-delay without auxiliary voltage <br> 1 change-over contact (RS-AR1) <br> 2 change-over contacts (RS-AR2) | Off-delay with auxiliary voltage 1 change-over contact (RS-AZ1) 2 change-over contacts (RS-AZ2) | Off-delay with auxiliary voltage <br> 1 change-over contact (RS-LA1) <br> 2 change-over contacts (RS-LA2) | Off-delay <br> 1 change-over contact |
| Pulse schedulel Function diagram |  |  |  |  |
| Wiring diagram |  |  |  |  |
| LED | 1 LED | 2 LEDs | 2 LEDs | nein |
| Timing ranges | $\begin{array}{\|lll} 0.05 & - & 1 \mathrm{~s} \\ 0.15 & - & 3 \mathrm{~s} \\ 0.5 & - & 10 \mathrm{~s} \\ 1.5 & - & 30 \mathrm{~s} \\ 3 & - & 60 \mathrm{~s} \\ 5 & -100 \mathrm{~s} \\ 15 & -300 \mathrm{~s} \\ 30 & -600 \mathrm{~s} \text { (not } \\ \text { possible with } 24 \mathrm{~V} \text { AC/DC) } \end{array}$ |  | $\begin{array}{llc} 1.5-30 \mathrm{~min} \\ 3 & - & 60 \mathrm{~min} \\ 0.15- & 3 \mathrm{~h} \\ 0.5 & -10 \mathrm{~h} \end{array}$ | $\begin{aligned} & \text { Fixed times: } \\ & 1 \mathrm{~s}, 3 \mathrm{~s}, 10 \mathrm{~s} \text {, } \\ & 30 \mathrm{~s}, 60 \mathrm{~s}, 100 \mathrm{~s}, \\ & 300 \mathrm{~s}, 600 \mathrm{~s} \end{aligned}$ |
| Dimensions | see diagram | see diagram | see diagram | see diagram |
| Exciting voltage | $\begin{gathered} 24 \vee \mathrm{AC} / \mathrm{DC} \\ 42-48 \mathrm{VAC} \\ 110-127 \mathrm{VAC} \\ 230 \vee \mathrm{AC} \end{gathered}$ | $\begin{array}{r} 12 \vee \mathrm{AC} / \mathrm{DC} \\ 24 \vee \mathrm{CC} / \mathrm{DC} \\ 42-48 \vee \mathrm{CC} \\ 110-127 \vee A C \\ 230 \end{array}$ | $\begin{array}{r} 12 \vee \mathrm{AC} / \mathrm{DC} \\ 24 \vee \mathrm{AC} / \mathrm{DC} \\ 42-48 \vee \mathrm{CC} \\ 110-127 \\ 230 \vee A C \\ 230 \end{array}$ | $\begin{array}{r} 12 \vee \mathrm{AC} / \mathrm{DC} \\ 24 \vee \mathrm{AC} / \mathrm{DC} \\ 42-48 \vee \mathrm{CC} \\ 110-127 \\ 230 \mathrm{AC} \\ \hline \end{array}$ |
| Tolerances | 4\% | 4\% | 4\% | 4\% |
| Attributes | CMOS technology Analog time setting <br> Absolute scale | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale Absolute scale | CMOS technology Fixed times |
| Description of function | These devices are off-delay time-lag relays without auxiliary voltage. After applying the exciting voltage to terminals A1 und A2 the output relay attracts instantaneously. If the exciting voltage is interrupted the output relay remains closed and the time begins. After expiring of the set time the output relay returns to its basic position. | These devices are release delaying relays under auxiliary current. They are controlled by a separate start contact potential-free. During operation of the device exciting voltage needs to be available at terminals A1 and A2. After closing the potential free start contact B1B 2 , the outlet relay will close immediately. After opening the start contact, the outlet relay remains closed and the timing will start. After the choosen time, the outlet relay goes back into its basic position.Voltage at terminals B1 and B 2 is 24 VDC . In closed position of the start contact current of approx. 10 mA will be meassured Min. operation time of the start contact is 5 ms . Caution: There is no galvanic isolation between connectors B1 or B2 and exciting voltage A1 and A2. | see col. 10 | see col. 10 |
| Column | 9 | 10 | 11 | 12 |

Time-delay Flashing

|  |  |  |
| :---: | :---: | :---: |
| Type | RS-BR1, RS-BR2 | RS-BRS1, RS-BRS2 |
| Function/ Contact | Flashing <br> 1 change-over contact (RS-BR1) <br> 2 change-over contacts (RS-BR2) | Flashing <br> 1 change-over contact (RS-BRS1) <br> 2 change-over contacts (RS-BRS2) |
| Pulse schedulel Function diagram |  |  |
| Wiring diagram |  |  |
| LED | 2 LEDs | 2 LEDs |
| Timing ranges |  | Variable clock - pulse times: $0.15-3 \mathrm{~s}$ |
| Dimensions | see diagram | see diagram |
| Exciting voltage | $12 \vee \mathrm{AC} / \mathrm{DC}$ $24 \vee \mathrm{AC} / \mathrm{DC}$ $42-48 \vee \mathrm{AC}$ $110 \vee \mathrm{AC}$ $230 \vee \mathrm{AC}$ | $\begin{aligned} & 12 \vee \mathrm{AC} / \mathrm{DC} \\ & 24 \vee \mathrm{AC} / \mathrm{DC} \\ & 42-48 \vee \mathrm{AC} \\ & 110 \vee \mathrm{AC} \\ & 230 \vee \mathrm{AC} \end{aligned}$ |
| Tolerances | 10\% | 10\% |
| Attributes | $\begin{aligned} & \hline \text { CMOS technology } \\ & \text { Analog time setting } \\ & \text { Absolute scale } \\ & \hline \end{aligned}$ | CMOS technology Analog time setting Absolute scale |
| Description of function | These devices are flashing relays with fixed interval/pulse ratios and fixed clockpulse time (RS-BR). After applying the exciting voltage to terminal A1 and A2 the timelag of the interval begins. The output relay remains in its basic position. After the interval has expired, the output relay attracts and remains in this switched position until the pulse time has expired. This procedure repeats in a cycle until the exciting voltage is interrupted. | These devices are flashing relays with fixed interval/pulse ratios and variable clock-pulse time. The time lag of the interval begins upon applying the exciting voltage to terminal A1 and A2. The output relay remains in its basic position. After the interval has expired, the output relay attracts and remains in this switched position until the pulse time has expired. This procedure repeats in a cycle until the exciting voltage is interrupted. |
| Column | 13 | 14 |

Time-delay Clock-pulsed

|  |  |  |
| :---: | :---: | :---: |
| Type | RS-TG1, RS-TG2 | RS-TGI1, RS-TGI2 |
| Function/ Contact | Clock-pulsed <br> 1 change-over contact (RS-TG1) <br> 2 change-over contacts (RS-TG2) | Clock-pulsed <br> 1 change-over contact (RS-TGI1) <br> 2 change-over contacts (RS-TGI2) |
| Pulse schedulel Function diagram |  |  |
| Wiring diagram |  |  |
| LED | 1LED | 1LED |
| Timing ranges | Pulse and interval times from any combination of the following: | Pulse and interval times from any combination of the following: |
| Dimensions | see diagram | see diagram |
| Exciting voltage | $\begin{array}{r} 24 \vee \mathrm{AC} / \mathrm{DC} \\ 42-48 \text { V AC } \\ 110-127 \text { V AC } \\ 230 \text { V AC } \end{array}$ | $\begin{array}{r} 24 \vee \mathrm{AC} / \mathrm{DC} \\ 42-48 \vee \mathrm{AC} \\ 110-127 \vee \mathrm{AC} \\ 230 \vee \mathrm{AC} \end{array}$ |
| Tolerances | 4\% | 4\% |
| Attributes | $\begin{array}{\|l} \hline \text { CMOS technology } \\ \text { Analog time setting } \\ \text { Relative scale } \\ \hline \end{array}$ | CMOS technology Analog time setting Relative scale |
| Description of function | These devices are repeat cycle timers with individually adjustable pulse and interval times. After applying the exciting voltage to terminal A1 and A2 the time-lag of the interval begins. The output relay remains in its basic position. After the interval has expired, the output relay attracts and remains in this switched position until the pulse time has expired. This procedure repeats in a cycle until the exciting voltage is interrupted. | These devices are repeat cycle timers with individually adjustable pulse and interval times. After applying the exciting voltage to terminal A1 and A2 the time-lag of the interval begins. The output relay attracts instantaneously and it remains in this switched position until the pulse time is over. After the pulse time has expired, the output relay returns to its basic position and the interval begins. This procedure repeats in a cycle until the exciting voltage is interrupted. |
| Column | 15 | 16 |

Time-delay Multifunction

|  |  |
| :---: | :---: |
| Type | RS-MF |
| Function/ Contact | Multifunction relay <br> 2 change-over contacts |
| Pulse schedulel Function diagram | Pulse schedule depends on function: <br> EV see col. 1 <br> AV see col. 12 <br> EW see col. 20 <br> TP see col. 15 <br> TI see technical data <br> EWI see technical data |
| Wiring diagram |  |
| LED | 2 LEDs |
| Timing ranges | 0.15 - 3 s <br> 0.6 - 12 s <br> 5 -100 s  <br> 40 -800 s  <br> or:   <br> 0.15 -3 min  <br> 0.6 -12 min  <br> 5 -100 min  <br> 40 -800 min  |
| Dimensions | see diagram |
| Exciting voltage | Exciting voltage 1 (A1-A2) $90-250$ V AC and exciting voltage 2 (A1-A3) 24 V AC/DC (only one voltage may be applied to the relay at the same time) |
| Tolerances | 4\% |
| Attributes | CMOS technology Analog time setting Relative scale |
| Description of function | ```The functions: EV = on-delay AV = off-delay with auxiliary voltage EW = making-pulse contact/ pulse clipping TP = clock-pulsed, starting with interval \(\mathrm{TI}=\) clock-pulsed, starting with pulse EWI = making-pulse contact/ pulse forming can be set by means of DIP-switches on the front.``` |
| Column | 17 |

Time-delay Interval-time-delay

|  |  |  |  |
| :---: | :---: | :---: | :---: |
| Type | RS-El1, RS-EI2 | RS-EIF | RS-WAR |
| Function/ Contact | Interval time-delay relay 1 change-over contact (RS-EI1) <br> 2 change-over contacts (RS-EI2) | Interval time-delay relay 1 change-over contact and remote connection | Interval time-delay relay 1 change-over contact (RS-WAR) |
| Pulse schedulel Function diagram |  |  |  |
| Wiring diagram |  |  |  |
| LED | 2 LEDs | 2 LEDs | no |
| Timing ranges | $\begin{array}{rlr} 0.05 & - & 1 \mathrm{~s} \\ 0.15 & - & 3 \mathrm{~s} \\ 0.5 & - & 10 \mathrm{~s} \\ 1.5 & - & 30 \mathrm{~s} \\ 3 & - & 60 \mathrm{~s} \\ 5 & - & 100 \mathrm{~s} \\ 15 & -300 \mathrm{~s} \\ 30 & -600 \mathrm{~s} \end{array}$ | $\begin{array}{rr} 0.05 & - \\ 0.15- & 1 \mathrm{~s} \\ 0.5 & - \\ 1.5 & - \\ 3 & -30 \mathrm{~s} \\ 3 & -60 \mathrm{~s} \\ 5 & -100 \mathrm{~s} \\ 15 & -300 \mathrm{~s} \\ 30 & -600 \mathrm{~s} \end{array}$ | Fixed time: $0.5 \mathrm{~s}$ |
| Dimensions | see diagram | see diagram | see diagram |
| Exciting voltage | $\begin{array}{r} 24 \vee \mathrm{AC} / D C \\ 42-48 \vee \mathrm{AC} \\ 110-127 \\ 230 \\ 230 \end{array}$ | $\begin{array}{r} 24 \vee \mathrm{AC} / \mathrm{DC} \\ 42-48 \vee \mathrm{AC} \\ 110-127 \vee \mathrm{AC} \\ 230 \vee \mathrm{AC} \\ \hline \end{array}$ | $\begin{gathered} 24 \mathrm{~V} \mathrm{AC/DC} \\ 110-127 \mathrm{VAC} \\ 230 \mathrm{VAC} \end{gathered}$ |
| Tolerances | 10\% | 10\% | 20\% |
| Attributes | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale | Analog time setting Fixed time |
| Description of function | These devices are electronic pulse-clipping relays. After applying the exciting voltage to terminal A1 and A2, the output relay attracts instantaneously and remains in this switched position for the set time. Then it returns to its basic position. | Resistance value for all time ranges is $10 \mathrm{~K} \Omega$. When connecting an external potentiometer for setting the time, the factory-fitted jumper has to be removed. The remote potentiometer has to be connected to the terminal Z1 and Z 2 . The integrated potentiometer has to be set to the shortest time. The lines to the external potentiometer can be up to 100 m long. Note: there is no electrical isolation between terminal Z1 and Z2 and the exciting voltage A1 and A2. | These devices are electronic pulse-clipping relays The functions: <br> EW = making-pulse contact <br> AW = breaking-pulse contact <br> and <br> EW/AW = making- and breaking-pulse contact <br> can be set via jumpers at terminals $\mathrm{X} 1, \mathrm{X} 2$ and X 3 . <br> $\mathrm{X} 1-\mathrm{X} 2=\mathrm{EW}$ <br> X2-X3 = AW <br> no jumper = EWIAW |
| Column | 18 | 19 | 20 |

Time-delay Star-delta

|  |  |  |
| :---: | :---: | :---: |
| Type | RS-SD | RS-SDS |
| Function/ Contact | Star-delta relay 1 change-over contact with delayed change-over | Star-delta relay 2 normaly closed contact with delayed change-over |
| Pulse schedulel Function diagram |  |  |
| Wiring diagram |  |  |
| LED | 2 LEDs | 2 LEDs |
| Timing ranges | $\begin{aligned} & 1.5-30 \mathrm{~s} \\ & 3-60 \mathrm{~s} \\ & 5-100 \mathrm{~s} \end{aligned}$ <br> Change-over times: $30,50,100 \mathrm{~ms}$ | $\begin{aligned} & 1.5-30 \mathrm{~s} \\ & 3-60 \mathrm{~s} \\ & 5-100 \mathrm{~s} \end{aligned}$ <br> Change-over times: $30,50,100 \mathrm{~ms}$ |
| Dimensions | see diagram | see diagram |
| Exciting voltage | $\begin{array}{r} 24 \vee \mathrm{AC} / \mathrm{DC} \\ 42-48 \vee \mathrm{AC} \\ 110-127 \vee \mathrm{AC} \\ 230 \vee \mathrm{AC} \\ \hline \end{array}$ | $24 \vee \mathrm{AC} / \mathrm{DC}$ $42-48 \vee \mathrm{AC}$ $110-127$ 230 |
| Tolerances | 4\% | 4\% |
| Attributes | CMOS technology Analog time setting Absolute scale | CMOS technology Analog time setting Absolute scale |
| Description of function | This is an on-delay time-lag relay with change over time. After applying the exciting voltage to terminal A1 and A2 the time-lag begins. The output relay remains in its basic position. After expiry of time, the output relay attracts with a change-over time and remains in this switched position until the exciting voltage is interrupted. If there is an interruption of the exciting voltage the relay returns to its basic position. | The RS-SDS is a time delayed relay four star-delta switching. After connecting the voltage supply to terminal A1 and A2, the NO-contact 1 (17-18) will close immediately and will stay in this position with the duration of the adjusted time tv1. At the end of the time tv1 and end of the switching time (selectable time of 30,50 and 100 ms ) the NOcontact 1 (17-18) opens and NOcontact 2 (27-28) close. NOcontact 2 keeps closed until disconnecting the voltage supply. After disconnecting the voltage supply the RS-SDS falls back in its basic position (NO-contact 1 and 2 are open). |
| Column | 21 | 22 |

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Technical specifications
Dimensions
Voltage drift

Temperature drift

Repeat accuracy

Reset time

Setting accuracy at
end of scale (tolerance)
Power consumption
Voltage tolerance range
Max. fuse rating
Duty cycle
Max. continuous current

Max. switching voltage
Max. switching rate Electrical life on load
Mechanical life
Contact material
Ambient temperature
Climatic resistance
Shock/Vibration resistance
Test voltage
Standards
Leakage paths / Air qaps
Operating position
Weight
Class of protection

Conductor
Mounting dimensions
Terminal markings
(of different housings and terminals)
+/- 0.001\% / \% $\Delta \mathrm{U}$ (CMOS technology)
$+/-0.2 \% / \% \Delta U$ (transistor technology)
+/- 0.02\% / $\triangle \circ \mathrm{C}$ (CMOS technology)
$+/-0.15 \% / \triangle^{\circ} \mathrm{C}$ (transistor technology)
+/- 0.5\% (CMOS technology)
+/- 2.0\% (transistor technology)
50 ms (CMOS technology)
250 ms (transistor technology)
+/- 4\%, +/- 10\%, +/- 20\% depending on device
< 7,5 VA
0.85-1.1 x Unom

10 A (fast)
100 \% continuous
10 A AC 1change-over contact
5 A AC 2 change-over contacts
1 A DC
0.5 A thyristor

250 V AC, $50 . .60 \mathrm{~Hz}, 250 \mathrm{~V}$ DC
250 V AC, $50 . .60 \mathrm{~Hz}$, thyristor
6000 operations/h
see diagram
$3 \times 10^{7}$ operations, $10^{8}$ thyristor
silver cadmium oxide / hard silver or equivalent material
$-25^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$
to DIN 40040, class F
5 g in all 3 directions, approx. 32 Hz
2500 V, 50 Hz
to DIN VDE 0435
to DIN VDE 0110-2
no restriction
approx. 120 g
Terminals and housings to DIN VDE 0470-1, housings IP 50,
terminals IP 20. Finger-touch and back-of-hand-touch protection
to VDE 0106/100 as well as VBG4.
$2 \times 2.5 \mathrm{~mm}^{2}$ solid or $2 \times 1.5 \mathrm{~mm}^{2}$ stranded with ferrules
to DIN EN 50022
to DIN EN 50005

Visual switching state (green) and voltage supply (red).
Initiator connection triggered via contact or 2-wire-proximity switch with residual current < 5mA.
Not available for the following types: RS-VR1, RS-VR2, RS-EB, RS-AZ1, RS-AZ2, RS-LA1, RS-LA2, RS-AZS, RS-MF,
RS-SD, RS-SDS.
We reserve the right to make changes to the technical specification
We were taking great care making up the texts and drawings. Nevertheless failures cannot be eliminated completely.
Release 2007

## Dimensions



Dimension



45 mm housing


Puls schedule for RS-MF:
Function EWI


Function TI


Contact diagram


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## Product features:

1.     + Mounting on standard 35 mm rails to DIN 46277 by means of snap-on fixings.

+ Possibility of additional screw fixings for 45 mm housings.

2.     + Connecting terminals in standard terminal position.

+ Captive plus/minus screws.
+ Class of protection to DIN VDE 0470-1, IP 20.
+ Finger-touch and back-of-hand-touch protection to VDE 0106/100 as well as VBG 4.
+ Conductors to DIN EN 40050.
+ Initiator connection (if available).
+ SEV prooved.

3.     + Self-extinguishing housing material to UL 94-V1.

+ Class of protection to DIN VDE 0470-1, IP 50.

4.     + Visual switching state and voltage supply indication by means of LED (not on RS-VR).
5.     + Wireless design.


## Outstanding quality

We not just test our relay after it`s finished we test it at every step of production! Our testing strategy thoroughly checks all functions of every single relay by means of a computerized testing system which we developed ourselves. We simulate situations in which the worse scenarious happen all at once. Only after such testing does a relay get the "thumbs-up". Therefore, "riese-relays" are ideally situated to rough environments, e.g. severe vibrations, temperature fluctuations or voltage discrepancies.

## Special features

1. 12 V relays, e.g. for vehicles, vehicle mountings We have developed a number of relays especially for this purpose. Not all types are included in this leaflet so please call us for details.
2. 24 V relays, e.g. for railways, tram systems. Peculiar to railways and tram systems is the fact that the voltage changes depending on the number of vehicles. We have managed to overcome these and other specific problems.

## 3. Brand-name labeling

Do you need relays with your company logo? No problem!
4. Special designs

If you can't find the relay you're looking for in our range, then please contact our design department. We relish the opportunity to discover new ways of optimizing your applications.

## Who or what is riese electronic?

Since 1958 riese electronic has been developing, purchasing materials worldwide, producing in SMD and wired technologies, and testing electronic components in line with customers' requirements. Time-delay, control and measuring relays bearing the "riese" name have been available since 1987, safety relays since 1991. Riese electronic employs 120 staff at it's two plants in Horb, Baden-Württemberg and

